

Titles of Most Frequently Occurring Classifications of Patents Returned From A Search of 10700351 on April 27, 2005

- 3 710/100 (3 OR, 0 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
- 3 710/110 (2 OR, 1 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
 710/107 .Bus access regulation
 710/110 ..Bus master/slave controlling
- 3 712/36 (1 OR, 2 XR)
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: PROCESSING ARCHITECTURES AND INSTRUCTION PROCESSING
 712/1 PROCESSING ARCHITECTURE
 712/32 .Microprocessor or multichip or multimodule processor having sequential program control
 712/36 ..Application specific
- 2 178/3 (2 OR, 0 XR)
 Class 178 : TELEGRAPHY
 178/2R SYSTEMS
 178/3 .Automatic
- 2 340/825.52 (1 OR, 1 XR)
 Class 340 : COMMUNICATIONS: ELECTRICAL
 340/825 SELECTIVE
 340/825.52 .Addressing
- 2 370/364 (0 OR, 2 XR)
 Class 370 : MULTIPLEX COMMUNICATIONS
 370/351 PATHFINDING OR ROUTING
 370/357 .Through a circuit switch
 370/360 ..Switching control
 370/362 ...Bus switch
 370/364Having plural buses
- 2 370/419 (0 OR, 2 XR)
 Class 370 : MULTIPLEX COMMUNICATIONS
 370/351 PATHFINDING OR ROUTING
 370/389 .Switching a message which includes an address header
 370/419 ..Input or output circuit, per se (i.e., line interface)
- 2 710/107 (1 OR, 1 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
 710/107 .Bus access regulation
- 2 710/260 (2 OR, 0 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
 710/260 INTERRUPT PROCESSING
- 2 710/263 (1 OR, 1 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
 710/260 INTERRUPT PROCESSING
 710/263 .Interrupt queuing
- 2 710/306 (1 OR, 1 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
 710/305 .Bus interface architecture
 710/306 ..Bus bridge
- 2 710/316 (2 OR, 0 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
 710/305 .Bus interface architecture
 710/316 ..Path selecting switch
- 2 711/121 (1 OR, 1 XR)
 Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
 711/100 STORAGE ACCESSING AND CONTROL
 711/117 .Hierarchical memories
 711/118 ..Caching
 711/119 ...Multiple caches
 711/121Private caches
- 2 711/141 (1 OR, 1 XR)
 Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
 711/100 STORAGE ACCESSING AND CONTROL
 711/117 .Hierarchical memories
 711/118 ..Caching
 711/141 ...Coherency
- 2 711/155 (0 OR, 2 XR)
 Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
 711/100 STORAGE ACCESSING AND CONTROL

711/154
711/155

.Control technique
..Read-modify-write (RMW)

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